

Vrijeme izvoza: 19.04.2024. 16:12:06

Repozitorij: dr.nsk.hr

Ukupan broj zapisa na URL-u: 10

Broj izvezenih zapisa: 10

Naslov	URL	Autori	Naslov izvornika
Relaxation oscillator architectures with delay and offset-voltage compensation		Mikulić, Josip	
Methodology for block-wise behavioural modelling of integrated circuits for electromagnetic compatibility applications		Magerl, Marko	
Characterization of power inductor losses and electromagnetic emissions of fast switching synchronous converters.		Bačmaga, Josip	
Design characterization of multiport electro-optical circuits for broadband voltage measurements in an electromagnetically polluted environment		Štimac, Hrvoje	
Design methodology for improved electromagnetic compatibility of switched-mode power converters		Blečić, Raul	
Circuit design of radio frequency identification system blocks in CMOS technology		Bako, Niko	
Modelling of Integrated Circuit Packages and Electromagnetic Coupling to Interconnects		Mandić, Tvrtko	
Susceptibility of integrated circuits to conducted electromagnetic interference		Jović, Ognjen	
Analysis of Conducted Electromagnetic Emissions in Integrated Circuits		Čeperić, Vladimir	
Određivanje gubitaka i nadomjesne temperature silicija učinkog bipolarnog tranzistora s izoliranom upravljačkom elektrodom		Bahun, Ivan	